29. The method as recited in claim 27 further comprising constructing a second packet containing at least one encoded block and forming a packetized bitstream of encoded graphic data.

30. The method as recited in claim 27 wherein each block comprises a pixel component.

31. The method as recited in claim 27 wherein each encoded block comprises encoded deltas wherein the deltas represent differences from a preceding block.

32. The method as recited in claim 1 wherein a scan line comprises an HDTV line.

33. A computer-readable medium bearing computer-readable instructions for carrying out the steps recited in claim 27.

REMARKS

Claims 1-26 are pending in the application. Claims 1, 2, 4-7, 9-14, 16-19, 21 and 23-25 were rejected. Claims 3, 8, 15, 20, and 22 are objected to.

The specification was objected to based on informalities. Applicant has corrected the informalities.

Claims 1, 2, 4-6, 13, 14, and 16-18 stand rejected under 35 U.S.C. § 103(a) as being unpatentable over Franaszek et al. (US 5,729,228) in view of Bigham (US 5,544,161).

In rejecting independent claims 1 and 13, the Office Action maintained that Franaszek teaches "the bitstream separated into blocks (b1 221, b2 222, b3 223, b4 224, called components)...." Office Action p. 2.

Applicant respectfully requests reconsideration of the rejection in view of the following:

Applicant maintains that Claim 1, for example, recites "separating a bitstream into a plurality of *components*; encoding the *components*...."

Claim 13, for example, recites "a plurality of encode units operable to receive components separated from a bitstream and to encode the components using a compression algorithm...."

Applicants submit that the claimed "components" of the present invention are defined by the specification in a manner different from the components of Franaszek. In determining the scope of a claim, the examiner must rely on the applicant's disclosure to properly determine the meaning of terms used in the claims. *Markman v. Westview Instruments*, 52 F.3d 967, 980 (Fed. Cir.) (*en banc*), *aff'd* U.S., 116 S.Ct. 1384 (1996) (The meaning of words used in a claim is not construed in a "lexicographic vacuum, but in the context of the specification and drawings.") See also MPEP 2106, II. C. Here, Applicants respectfully submit that Examiner has not properly construed the phrase "component" and that a proper construction of the phrase "component" would overcome the art of record. The specification makes clear that the meaning of component in this context is that "[p]ixels generally consist of one or more *components*" See e.g., P. 17, ll. 7-8. Hence a component is an object that makes up a pixel (i.e., a picture element).

By contrast, the Suver reference relied upon by the Examiner teaches

nothing of encoding pixels or data containing pixels, but rather teaches that

"[b]lock B is logically divided into four equal size components, referred to as sub-

blocks b1 (221), b2 (222), b3 (223), b4 (224)."

For at least the foregoing reasons, Applicant respectfully requests reconsideration

of the 103(a) rejection with respect to independent claims 1 and 13. Applicants also

respectfully request reconsideration of the rejection of dependent claims 2, 4-6, 14, and

16-18 by virtue of their respective incorporation of the limitations of the independent

claims from which they depend.

Claims 7, 9-12, 19, 21, and 23-26 were rejected under 35 USC 103(a) as being

unpatentable over Franaszek et al. in view of Bigham and Schwartz et al.

Inasmuch as claims 7, 9-12, 19, 21, and 23-26 depend from independent claims 1

and 13, applicants submit that they also define over Franaszek at least for the reasons set
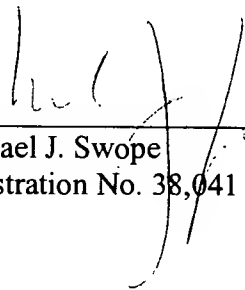
forth above.

CONCLUSION

A Notice of Allowance for claims 1-33 is respectfully solicited.

Attached hereto is a marked-up version of the changes made to the specification

and claims by the current amendment. The attached page is captioned **"Version with**

markings to show changes made."

Respectfully submitted,

Date: 1/15/-2

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VERSION WITH MARKINGS TO SHOW CHANGES MADE

In the specification:

Please change the paragraph beginning on page 22, line 15, as follows:

FIGURE 8 is a block diagram of one embodiment of a parallel decoder system, indicated generally at 60, according to the present invention. Encoder system 60 comprises a feeder (distributor) 62 that feeds a plurality of decode queues (FIFO) 64. A plurality of decode units 66 are each associated with one of decode queues [65] 64. Decode units [64] 66, in turn, feed a pixel sync 68 which provides decoded pixel outputs. Decode units [64] 66 can be associated with processing packets for particular interleaved lines, as shown. Decoder system 60 can be implemented to process a bitstream parallelized using fixed-length packets or using variable-length packets. As above, for fixed-length packets, each decode queue 64 and decode unit 66 pair can be associated with particular lines (e.g., 0, N, 2N, . . .). For variable-length packets, each decode queue 64 and decode unit 66 pair can be associated with particular components and component types (e.g., 0, N, 2N, . . .).

In the claims:

Please add new claims 27-33 as follows:

--27. A method for parallel compression of graphic data, comprising:

separating a bitstream into a plurality of scan lines;

encoding each scan line into a plurality of blocks using a lossless compression algorithm; and

constructing at least one packet containing at least on encoded block.

28. The method as recited in claim 27 wherein the lossless compression algorithm comprises differential pulse code modulation.

29. The method as recited in claim 27 further comprising constructing a second packet containing at least one encoded block and forming a packetized bitstream of encoded graphic data.

30. The method as recited in claim 27 wherein each block comprises a pixel component.

31. The method as recited in claim 27 wherein each encoded block comprises encoded deltas wherein the deltas represent differences from a preceding block.

32. The method as recited in claim 1 wherein a scan line comprises an HDTV line.

33. A computer-readable medium bearing computer-readable instructions for carrying out the steps recited in claim 27.--